

Definition: Routing in a VLSI design is the process of determining and prescribing paths between various electronic components in order to establish the connection between a given source node and its target.

Tessellated into rectangular array.


Objective: To maximize no of routed nets (pair of source-target pins) while having the minimum energy inside the routing region.

Motivation: Many routing methods that deploy shortest path strategy reported in the literature without considering the placement of obstacles. The presence of obstacles complicates the routing process as its limits the number of communication links.

Route for certain net might take longer or sometimes impossible to complete.


Net Ordering Problem: Net ordering problem plays an important role in producing high quality routing.


N1, N2, N3. E=31


N3, N1, N2. E=27

## ROUTING METHODS

- Simulated Annealing Method
- Worst result will not be rejected directly.
- It will be consider under Boltzmann Probability Function

$$
P(\Delta E)=e^{\frac{-\Delta E}{T_{i}}}
$$



In conclusion, it was shown that our proposed method able to provide good results with the presence of obstacles. The proposed algorithm can be applied and extended to any sequential routing problem.

## RELATED PUBLICATIONS

[1] Adzhar N and Salleh S 2014 MESH ROUTING: Maximing number of connections using heuristic method. Proc. International Conference on the Analysis \& Mathematical Applications in Engineering \& Science p 161
[2] Adzhar $N$ and Salleh 52014 Simulated Annealing Technique For Routing In A Rectangular Mesh Network. Modelling and Simulation in Engineering
[3] Adzhar N and Salleh S 2015 Obstacle-Aware Routing Problem In A Rectangular Mesh Network. Applied Mathematical Sciences 914

